

# WEST Search History





DATE: Monday, November 27, 2006

**Hide? Set Name Query****Hit Count***DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR*

<input type="checkbox"/>	L46	L45 and casc	28
<input type="checkbox"/>	L45	memory adj chain	229
<input type="checkbox"/>	L44	bataille.xa,xp. and input with (connect\$ or coupl\$) with output with memory	69
<input type="checkbox"/>	L43	bataille.xp. and casc	7
<input type="checkbox"/>	L42	L41 and casc	7
<input type="checkbox"/>	L41	bataille.xa.	186
<input type="checkbox"/>	L40	L39 not l33	29
<input type="checkbox"/>	L39	bataille.xa,xp. and chain and seri\$	31
<input type="checkbox"/>	L38	bataille and (chain).ti,ab.	12
<input type="checkbox"/>	L37	bataille and (seri\$ and memory).ti,ab.	8
<input type="checkbox"/>	L36	bataille and (seri\$ and memory)ti,ab.	279
<input type="checkbox"/>	L35	bataille and casc\$.ti,ab.	2
<input type="checkbox"/>	L34	L33 or l32	32
<input type="checkbox"/>	L33	bataille.xp,xa. and casc	14
<input type="checkbox"/>	L32	bataille.xp,xa. and memory near2 seri\$	18
<input type="checkbox"/>	L31	bataille.xp,xa. and memory near2 seri\$.ti,ab.	2
<input type="checkbox"/>	L30	bataille.xp,xa. and casc\$.ti,ab.	2
<input type="checkbox"/>	L29	bataille.xp,xa. and casc\$ with memory.ti,ab.	1
<input type="checkbox"/>	L28	l27 or l24	7
<input type="checkbox"/>	L27	20020083255	1
<input type="checkbox"/>	L26	20020083244	1
<input type="checkbox"/>	L25	(5715274 5860080 5945886 6125419 6304450 20010067915 20020083255 6833984).pn.	7
<input type="checkbox"/>	L24	(5715274 5860080 5945886 6125419 6304450 20010067915 20020083255 6833984).pn.	7
<input type="checkbox"/>	L23	L22 and 711/\$.ccls.	55
<input type="checkbox"/>	L22	memory with seri\$.ti,ab. and memory with series with controller	242
<input type="checkbox"/>	L21	L20 and L19	4
<input type="checkbox"/>	L20	L18 and point?to?point	15
<input type="checkbox"/>	L19	L18 and input near3 port and output	32
<input type="checkbox"/>	L18	memory near5 chain.ti,ab.	255
<input type="checkbox"/>	L17	(6917546 6684354).pn.	2

*DB=USOC,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=OR*

<input type="checkbox"/>	L16	L15 not L13	32
<input type="checkbox"/>	L15	L14 and dram	34

<input type="checkbox"/>	L14	(matsui.in. or yoshinori.in.) and memory and 2002	386
<input type="checkbox"/>	L13	L8 and 2002	10
<input type="checkbox"/>	L12	L8 and "august 23, 2002"	0
<input type="checkbox"/>	L11	L8 and 244322	0
<input type="checkbox"/>	L10	L8 and \$244322	60
<input type="checkbox"/>	L9	L8 and dram	11
<input type="checkbox"/>	L8	L6 and yoshinori.in.	65
<input type="checkbox"/>	L7	L6 and chain	4
<input type="checkbox"/>	L6	matsui.in. and memory	1690
		<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L5	matsui.in. and memory	1290
		<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L4	matsui.in. and memory near4 chain	0
		<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L3	L2 and access near3 command	14
<input type="checkbox"/>	L2	L1 and sequence and controller	109
<input type="checkbox"/>	L1	point?to?point and memory near4 chain	182

END OF SEARCH HISTORY

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<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L43	L42 and L41	4
<input type="checkbox"/>	L42	L40 and point?to?point	15
<input type="checkbox"/>	L41	L40 and input near3 port and output	32
<input type="checkbox"/>	L40	memory near5 chain.ti,ab.	255
<input type="checkbox"/>	L39	(6917546 6684354).pn.	2
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L38	L37 not L35	32
<input type="checkbox"/>	L37	L36 and dram	34
<input type="checkbox"/>	L36	(matsui.in. or yoshinori.in.) and memory and 2002	386
<input type="checkbox"/>	L35	L30 and 2002	10
<input type="checkbox"/>	L34	L30 and "august 23, 2002"	0
<input type="checkbox"/>	L33	L30 and 244322	0
<input type="checkbox"/>	L32	L30 and \$244322	60
<input type="checkbox"/>	L31	L30 and dram	11
<input type="checkbox"/>	L30	L28 and yoshinori.in.	65
<input type="checkbox"/>	L29	L28 and chain	4
<input type="checkbox"/>	L28	matsui.in. and memory	1690
	<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L27	matsui.in. and memory	1290
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L26	matsui.in. and memory near4 chain	0
	<i>DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L25	L24 and access near3 command	14
<input type="checkbox"/>	L24	L23 and sequence and controller	109
<input type="checkbox"/>	L23	point?to?point and memory near4 chain	182
<input type="checkbox"/>	L22	5404464.pn.	1
<input type="checkbox"/>	L21	20020009028.PN. AND SPARE and tempora\$ and change\$4	1
<input type="checkbox"/>	L20	20020009028.PN. AND SPARE and tempora\$	1
<input type="checkbox"/>	L19	20020009028.PN. AND SPARE	1
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; THES=ASSIGNEE; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L18	wear near2 level\$ with (flash or EEPROM or memory)	29
<input type="checkbox"/>	L17	wear near2 balanc\$ with (flash or EEPROM or memory)	2
<input type="checkbox"/>	L16	wear near3 level\$.ti,ab. and version and (backup or mirror\$.ti,ab.	0
<input type="checkbox"/>	L15	wear near4 level\$.ti,ab,clm. with flash	15

*DB=PGPB,USPT; THES=ASSIGNEE; PLUR=YES; OP=OR*

<input type="checkbox"/>	L14	L13 and (backup or mirror\$)	8
<input type="checkbox"/>	L13	wear near4 level\$.ti,ab,clm. with flash	41
<input type="checkbox"/>	L12	(backup or mirror\$).ti,ab. and wear near4 level\$.ti,ab,clm.	2
<input type="checkbox"/>	L11	L10 and (backup or mirror\$)	5
<input type="checkbox"/>	L10	wear near4 balanc\$.ti,ab. not l1	61
<input type="checkbox"/>	L9	wear near3 level\$.ti,ab. and version and (backup or mirror\$).ti,ab.	0
<input type="checkbox"/>	L8	L6 and flash not l7	39
<input type="checkbox"/>	L7	L6 and flash.ti,ab.	16
<input type="checkbox"/>	L6	wear near3 level\$ and version and (backup or mirror\$)	117
<input type="checkbox"/>	L5	L4 and version	4
<input type="checkbox"/>	L4	L3 and wear.ti,ab.	35
<input type="checkbox"/>	L3	L2 and flash	295
<input type="checkbox"/>	L2	wear near2 level\$ with (flash or EEPROM or memory)	305
<input type="checkbox"/>	L1	wear near2 balanc\$ with (flash or EEPROM or memory)	5

END OF SEARCH HISTORY